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PADDEDS	0
ALLOCATION	65476
ALLOCATIONS	7950
((FRONT ADJ4 PADDED) ADJ4 ALLOCATION).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	0
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<u>L33</u>	front adj4 padded adj4 allocation	0	<u>L33</u>
<u>L32</u>	130 same (permission or protect or protection or protecting)	3	<u>L32</u>
<u>L31</u>	130 same (bound or boundary)	1	<u>L31</u>
<u>L30</u>	capability pointer	66	<u>L30</u>

<a href="#"><u>L29</u></a>	L28 and l27	19	<a href="#"><u>L29</u></a>
<a href="#"><u>L28</u></a>	((page or block) adj2 size) same (number adj2 (page or block)) same segment	161	<a href="#"><u>L28</u></a>
<a href="#"><u>L27</u></a>	(multiple or variable or varying) adj2 (page or block) adj2 (size or length)	1248	<a href="#"><u>L27</u></a>
<a href="#"><u>L26</u></a>	((block or page) adj3 size) same (('number of blocks') or ('number of pages')) same (identifier or address or page number) same offset	0	<a href="#"><u>L26</u></a>
<a href="#"><u>L25</u></a>	((block or page) adj3 size) same (number adj2 (block or page)) same (identifier or address or page number) same offset	130	<a href="#"><u>L25</u></a>
<a href="#"><u>L24</u></a>	5845331[uref]	14	<a href="#"><u>L24</u></a>
<a href="#"><u>L23</u></a>	l20 same segment	13	<a href="#"><u>L23</u></a>
<a href="#"><u>L22</u></a>	l20 same capability	13	<a href="#"><u>L22</u></a>
<a href="#"><u>L21</u></a>	L20 and l19	3	<a href="#"><u>L21</u></a>
<a href="#"><u>L20</u></a>	(pointer or identifier or ID or descriptor) same size same length same (permission or protect)	88	<a href="#"><u>L20</u></a>
<a href="#"><u>L19</u></a>	(((711/2??))!.CCLS.)	4333	<a href="#"><u>L19</u></a>
<a href="#"><u>L18</u></a>	((711.2??)!.CCLS.)	0	<a href="#"><u>L18</u></a>
<a href="#"><u>L17</u></a>	(object adj (capability or capabilities)) same (permission or protect or protection)	12	<a href="#"><u>L17</u></a>
<a href="#"><u>L16</u></a>	object adj (capability or capabilities)	945	<a href="#"><u>L16</u></a>
<a href="#"><u>L15</u></a>	(descriptor or capability or pointer) same segment same (bound or limit) same permission	16	<a href="#"><u>L15</u></a>
<a href="#"><u>L14</u></a>	(descriptor or capability or pointer) same segment same bound same permission same (size or length) same (number or amount)	0	<a href="#"><u>L14</u></a>
<a href="#"><u>L13</u></a>	capability same segment same permission same bound	2	<a href="#"><u>L13</u></a>
<a href="#"><u>L12</u></a>	capability pointer	66	<a href="#"><u>L12</u></a>
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2. [Hardware Support for Fast \*\*Capability-based Addressing\*\* \(PDF\)](#)   
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3. [\[CST-2\] \[AST\] \*\*Capability Based Addressing\*\*](#)   
[CST-2] [AST] **Capability Based Addressing**. Pete Campton [cst-2@srcf.ucam.org](mailto:cst-2@srcf.ucam.org) 27 May 2003 14:43:46 +0100: ... Next message: [CST-2] [AST] **Capability Based Addressing**; ...   
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4. [Capability](#)   
**Capability-Based Addressing**. RS Fabry @ UC-Berkeley. Communications of the ACM, July 1974, pages 403-412. Introduction. **Capability**: absolute ...   
[www.cs.wisc.edu/~sschang/OS-Qual/memory/capability.htm](http://www.cs.wisc.edu/~sschang/OS-Qual/memory/capability.htm) [cached](#)
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... Hardware Support for Fast **Capability-based Addressing**. Proc. ASPLOS VI, Oct. ... **Capability-based addressing**. Communications of the ACM, 17,7, July 1974, pp. ...   
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... **capability** for clients to cancel previous subscriptions and advertisements. We discuss how these embellishments impact the model of content-based **addressing** ...   
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Object-oriented Issues - Literature Review Nierstrasz (Correct)

....reliability, resilience, recovery and fault tolerance. **Haer83 Kohl81 Oki83 Shin84 Svob84 Verh78]** Security: Security issues for objects seem to be well addressed by capabilities. **The** Hydra system contains some interesting ideas. **The** Landwehr paper is a survey of various techniques and paradigms. **[Cohe75 Fabr74 Land81 Wulf74]** **Surveys: The following are surveys of various object oriented systems and concepts, or special issues.** See also concepts. **Benn82 BYTE86 Nier86 Nyga86 Obj85 Obj86 Oops86 Stef85 Stoy84]** Transactions: The notion of transactions and atomic actions, particularly nested transactions, is relevant to ....

R.S. Fabry, "*Capability-Based Addressing*", *CACM*, vol. 17, no. 7, pp. 403-412, July 1974.

---

Introducing Hybrid: A Unified Object-Oriented System - Nierstrasz Institute Of (Correct)

....locking is typically used to implement this sort of behaviour [Moss81, Verh78] The object manager is also responsible for bringing needed objects into memory and resolving object identifiers to actual memory addresses. **Object identifiers can be thought of as capabilities for addressing objects [Fabr74].** Oids are actually capabilities for sending messages, i.e. for performing operations. **One** thus distinguishes not merely between, say, read and write operations, but between all of the various operations supported by an object. **This** is the same philosophy adopted in the Hydra operating system ....

R.S. Fabry, "*Capability-Based Addressing*", *CACM*, vol. 17, no. 7, pp. 403-412, July 1974.

---

Adaptive and Resilient Security for Multi-hop Multi-media Mobile.. - Kong (Correct)

....well proven concept, the capability. **Suppose** all operations concerned have already been approved by the access control model; in this case a network entity can choose to handoff a session to a specific set of network entities by securely delivering its session capability to them. **A capability [19, 22] is formally defined as an unforgeable pair made up of (i) a unique object identifier and (ii) a set of authorised operations as the interface associated with the object.** Security in a capability based model is enforced by three properties: i) capabilities are unforgeable and tamper proof; ii) ....

R. Fabry. *Capability-Based Addressing*. Communication of the ACM, 17(7):403–412, 1974.

---

Sparsely Faceted Arrays: A Mechanism Supporting Parallel.. - Brown (2002) (1 citation) (Correct)

....nodes. **One** notable departure from the conventional approach is the J machine [51] parallel computer. **In the J machine, all references to objects, distributed or otherwise, are indirected through a segment table on each node; this style of addressing is similar to that used by early capability [14] architectures [39]** Using indirection tables allows the J machine to provide distributed objects with arbitrary, globally unique names. The COSMOS operating system [25] uses the J machine's translation tables to name aggregate objects. Aggregate objects are composed of representative objects ....

R. S. Fabry. *Capability-based addressing*. *Communications of the ACM*, 17(7):403– 12, July 1974.

---

Sparsely Faceted Arrays: A Mechanism Supporting Parallel.. - Brown (2002) (1 citation) (Correct)

....nodes. 17 **One** notable departure from the conventional approach is the J machine [51] parallel computer. **In the J machine, all references to objects, distributed or otherwise, are indirected through a segment table on each node; this style of addressing is similar to that used by early capability [14] architectures [39]**


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## **Capability**

... Solution 4: **Capability Addressing**. ... Machines which are potential to support **capability addressing** but lacks of supporting OS are not sufficient either. ...

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... **Addressing** the Demand for Advanced Analytical **Capability**. By Craig Schiff,